

Notice of References Cited

Application/Control No.

09/212,915

Applicant(s)/Patent Under
Reexamination
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Examiner

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Art Unit

2823

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,166,083	11-1992	Bayraktaroglu, Burhan	148/DIG.72
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Translation of Japanese Patent No. 63-261833, "Manufacture of Semiconductor Substrate" by Chikara Yamaguchi, et al. PTO 96-0835.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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